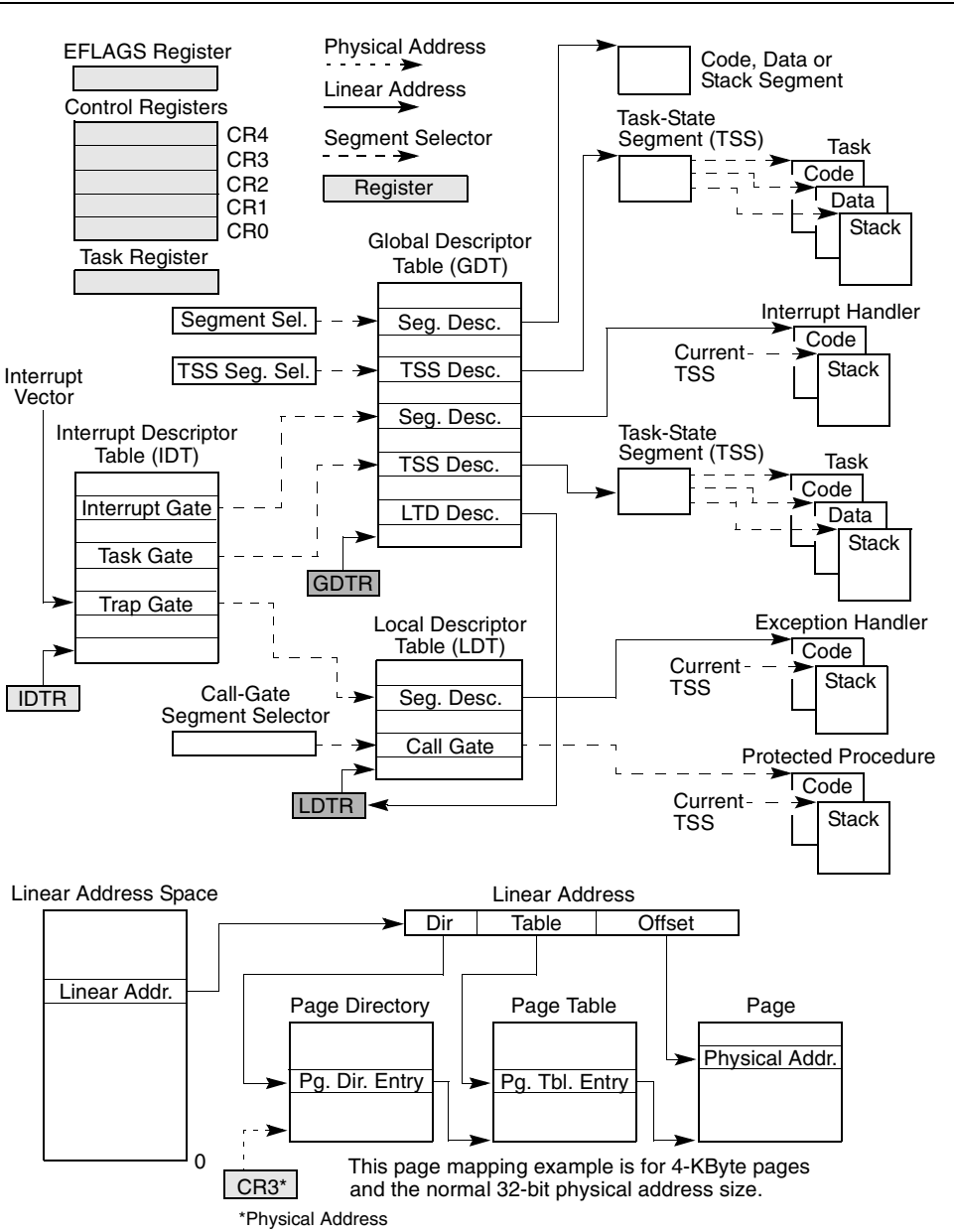

Figure 2-1. IA-32 System-Level Registers and Data Structures

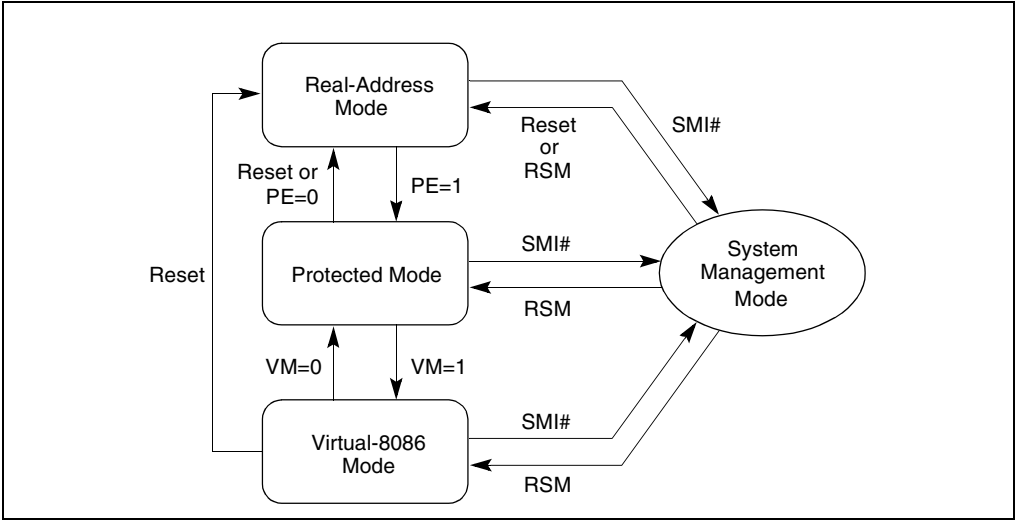

Figure 2-2. Transitions Among the Processor's Operating Modes

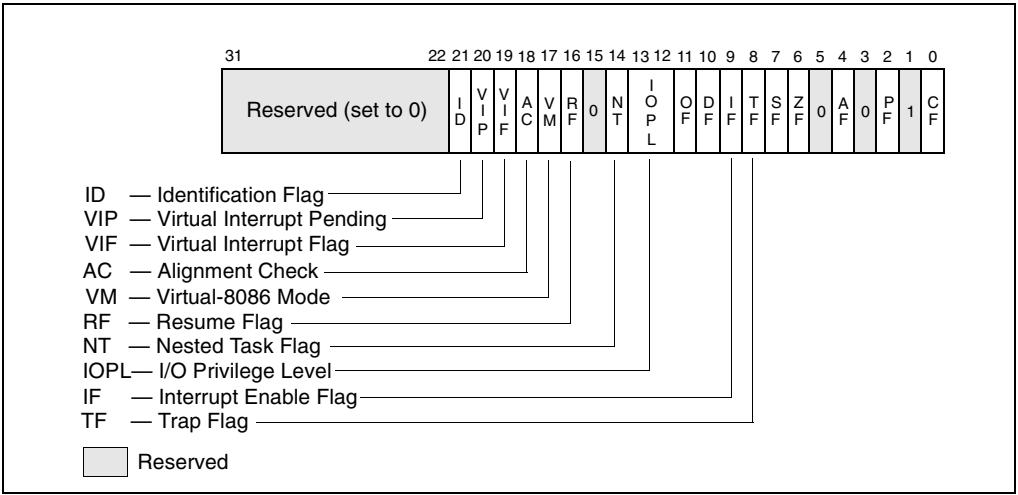

Figure 2-3. System Flags in the EFLAGS Register

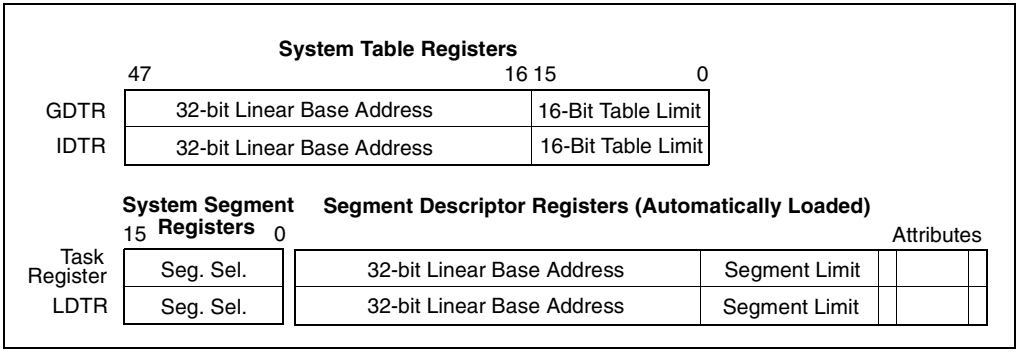

Figure 2-4. Memory Management Registers

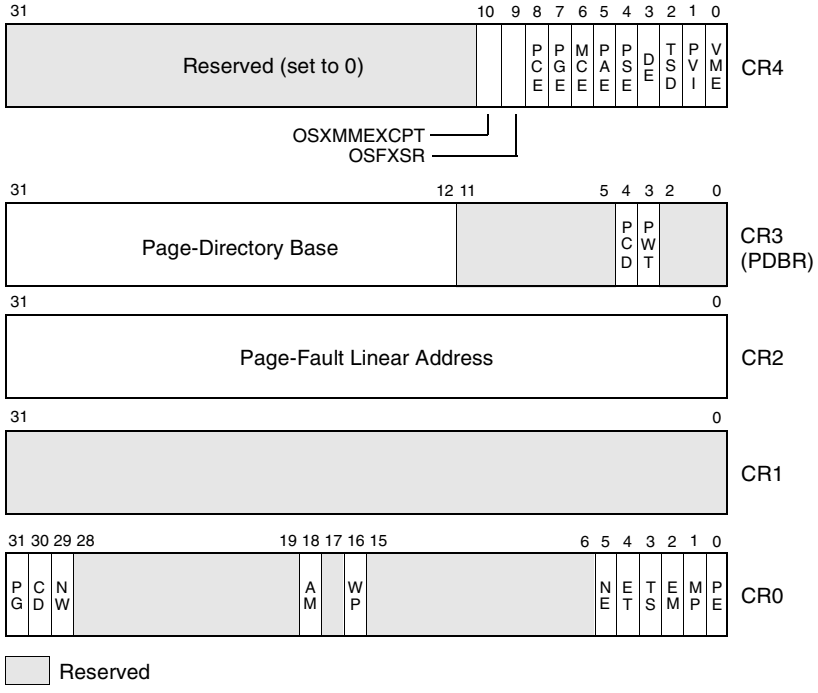

Figure 2-5. Control Registers